

U S DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE FORMS PTO-1390 (REV 10-96)		ATTORNEY'S DOCKET NUMBER SP00-191
TRANSMITTAL LETTER TO THE UNITED STATES DESIGNATED/ELECTED OFFICE (DO/EO/US) CONCERNING A FILING UNDER 35 U.S.C. 371		U.S. APPLICATION NO. (If known, see 37 CFR 1.5) To 10/048138 Be Assigned
INTERNATIONAL APPLICATION NO. PCT/US00/18798	INTERNATIONAL FILING DATE 10 July 2000	PRIORITY DATE CLAIMED 22 July 1999
<b>TITLE OF INVENTION</b> Extreme Ultraviolet Soft X-Ray Projection Lithographic Method System and Lithography Elements		
<b>APPLICANT(S) FOR DO/EO/US</b> Corning Incorporated		
Applicant herewith submits to the United States Designated/Elected Office (DO/EO/US) the following items and other information:		
<ol style="list-style-type: none"> <li>1. <input checked="" type="checkbox"/> This is a <b>FIRST</b> submission of items concerning a filing under 35 U.S.C. 371.</li> <li>2. <input type="checkbox"/> This is a <b>SECOND</b> or <b>SUBSEQUENT</b> submission of items concerning a filing under 35 U.S.C. 371.</li> <li>3. <input type="checkbox"/> This express request to being national examination procedures (35 U.S.C. 371(f)) at any time rather than delay examination until the expiration of the applicable time limit set in 35 U.S.C. 371(b) and PCT Articles 22 and 39(1).</li> <li>4. <input checked="" type="checkbox"/> A proper Demand for International Preliminary Examination was made by the 19<sup>th</sup> month from the earliest claimed priority date.</li> <li>5. <input checked="" type="checkbox"/> A copy of the International Application as filed (35 U.S.C. 371(c)(2))             <ol style="list-style-type: none"> <li>a. <input type="checkbox"/> is transmitted herewith (required only if not transmitted by the International Bureau).</li> <li>b. <input checked="" type="checkbox"/> has been transmitted by the International Bureau.</li> <li>c. <input type="checkbox"/> is not required, as the application was filed in the United States Receiving Office (RO/US).</li> </ol> </li> <li>6. <input type="checkbox"/> A translation of the International Application into English (35 U.S.C. 371(c)(2)).</li> <li>7. <input checked="" type="checkbox"/> Amendments to the claims of the International Application under PCT Article 19 (35 U.S.C. 371(c)(3)).             <ol style="list-style-type: none"> <li>a. <input type="checkbox"/> are transmitted herewith (required only if not transmitted by the International Bureau).</li> <li>b. <input type="checkbox"/> have been transmitted by the International Bureau.</li> <li>c. <input checked="" type="checkbox"/> have not been made; however, the time limit for making such amendments has NOT expired.</li> <li>d. <input type="checkbox"/> have not been made and will not be made.</li> </ol> </li> <li>8. <input type="checkbox"/> A translation of the amendments to the claims under PCT Article 19 (35 U.S.C. 371(c)(3)).</li> <li>9. <input checked="" type="checkbox"/> An oath or declaration of the inventor(s) (35 U.S.C. 371(c)(4)).</li> <li>10. <input type="checkbox"/> A translation of the annexes to the International Preliminary Examination Report under PCT Article 36 (35 U.S.C. 371(c)(5)).</li> </ol>		
<b>Items 11. To 16. Below concern document(s) or information included:</b>		
<ol style="list-style-type: none"> <li>11. <input type="checkbox"/> An Information Disclosure Statement under 37 CFR 1.97 and 1.98.</li> <li>12. <input checked="" type="checkbox"/> An Assignment document for recording. A separate cover sheet in compliance with 37 CFR 3.28 and 3.31 is included.</li> <li>13. <input type="checkbox"/> A <b>FIRST</b> preliminary amendment.</li> <li>14. <input type="checkbox"/> A <b>SECOND</b> or <b>SUBSEQUENT</b> preliminary amendment.</li> <li>15. <input type="checkbox"/> A change of power of attorney and/or address letter.</li> <li>16. <input type="checkbox"/> Other items or information:</li> </ol>		

10/048138

531 Rec'd PCT/PI 22 JAN 2002

17.  The following fees are submitted:**BASIC NATIONAL FEE (37 CFR 1.492 (a)(1)-(5):**

Neither international preliminary examination fee (37 CFR 1.482)  
nor international search fee (37 CFR 1.445(a)(2)) paid to USPTO  
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International preliminary examination fee (37 CFR 1.482) not paid to  
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Surcharge of **\$130.00** for furnishing the oath or declaration later than  20  30 months from  
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CLAIMS	NUMBER FILED	NUMBER EXTRA	RATE	
Total claims	43 - 20 =	23	X \$18.00	\$414.00
Independent claims	5 - 3 =	2	X \$84.00	\$168.00
MULTIPLE DEPENDANT CLAIM(S) (if applicable)			+ \$270.00	\$

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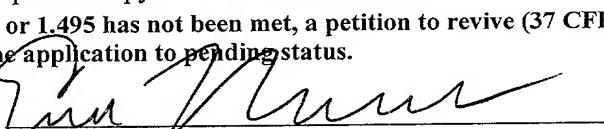
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1.137(a) or (b)) must be filed and granted to restore the application to pending status.**

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**EXTREME ULTRAVIOLET SOFT X-RAY PROJECTION  
LITHOGRAPHIC METHOD SYSTEM AND LITHOGRAPHY ELEMENTS**

**CROSS-REFERENCE TO RELATED PATENT APPLICATIONS**

5        This application claims the benefit of U.S. Provisional Application Serial Number No 60/145,057, filed July 22, 1999 entitled Extreme Ultraviolet Soft X-Ray Projection Lithographic Method and Mask Devices of Claude L. Davis, Robert Sabia and Harrie J. Stevens, and U.S. Provisional Application Serial Number No 60/149,840, filed August 19, 1999 entitled Extreme Ultraviolet Soft X-Ray Projection Lithographic Method and Mask Devices of Claude L. Davis, Robert Sabia, Harrie J. Stevens and Kenneth E. Hrdina, and U.S. Provisional Application Serial Number No 60/158,813, filed October 12, 1999 entitled Extreme Ultraviolet Soft X-Ray Projection Lithographic Method and Mask Devices of Claude L. Davis and Kenneth E. Hrdina.

10      15

**BACKGROUND OF THE INVENTION**

**FIELD OF THE INVENTION**

20      The present invention relates generally to projection lithographic methods and systems for producing integrated circuits and forming patterns with extremely small feature dimensions. The present invention relates particularly to extreme ultraviolet soft x-ray based lithography and radiation manipulating lithography elements which reflectively operate on and manipulate extreme ultraviolet soft x-ray radiation to illuminate, project, and reduce pattern images that are utilized to form circuit patterns.

The present invention relates to lithography elements and their use for reflecting extreme ultraviolet soft x-ray photons to enable the use of extreme ultraviolet soft x-ray radiation for lithography that surpasses current optical lithography circuit feature dimension performance.

5

### TECHNICAL BACKGROUND

The use of extreme ultraviolet soft x-ray radiation provides benefits in terms of achieving smaller feature dimensions but due to the nature of the radiation, it presents difficulties in terms of manipulating and directing such wavelengths of radiation and has delayed the commercial manufacturing use of such radiation. Current optical lithography systems used in the manufacturing of integrated circuits have progressed towards shorter optical wavelengths of light, such as from 248 nm to 193 nm to 157nm, but the commercial use and adoption of extreme ultraviolet soft x-rays has been hindered. Part of this slow progression to very short wavelengths of radiation such as in the 15 nm range, has been due to the lack of economically manufacturable mirror elements that can withstand the exposure to such radiation while maintaining a stable and high quality circuit pattern image. For the benefits of extreme ultraviolet soft x-rays to be utilized in the manufacturing of integrated circuits, there is a need for stable high quality glass lithography elements, that preferably allow for direct deposition of reflective coatings to the surface of the glass substrate body.

United States Patent No. 5,956,192 describes a four mirror EUV projection system with lithography mirror elements including two convex mirror elements and two concave mirror elements. United States Patent No. 5,353,322 describes a three mirror x-ray projection lens system with lithography mirror elements including a convex mirror and two concave mirrors. As noted from United States Patent No. 5,698,113, current extreme ultraviolet soft x-ray lithographic systems are extremely expensive. United States Patent No. 5,698,113 tries to address such high costs by trying to recover the surfaces of multilayer coated substrates by etching the multilayer reflective coatings from substrate surfaces of fused silica and ZERODUR type aluminosilicate crystalline glass-ceramics, even though such etching degrades the substrate surface. "Extreme Ultraviolet Lithography" by Richard Stulen and Donald Sweeney, Optics and Photonics

A vertical column of small, illegible marks or symbols is located on the left margin of the page, approximately between lines 35 and 600.

News, August 1999, Vol. 10, No. 8, disclose an EUV reflective coating on a Si substrate (Figure 3).

The present invention provides for economically manufactured lithography element substrates that are stable, ready and receptive to receiving multilayer reflective coatings and provide an improved extreme ultraviolet soft x-ray based projection lithography method/system. The present invention provides a stable high performance lithography element with the reflective multilayer coating directly deposited on the finished glass surface, and avoids costly and cumbersome intermediate layers between the glass substrate surface and the reflective multilayer coating.

10

#### SUMMARY OF THE INVENTION

One aspect of the present invention is a projection lithographic method/system for producing integrated circuits with printed feature dimensions less than 100 nm, preferably  $\leq$  70 nm, and most preferably  $\leq$  50 nm, that includes providing an illumination sub-system for producing and directing an extreme ultraviolet soft x-ray radiation  $\lambda$  from an extreme ultraviolet soft x-ray source. The method further includes providing a mask stage and a mask illuminated by the extreme ultraviolet soft x-ray radiation  $\lambda$  produced by the illumination sub-system for forming a projected mask pattern when illuminated by radiation  $\lambda$ . The method further includes providing a projection sub-system, a wafer stage and an integrated circuit wafer which has a radiation sensitive wafer surface wherein the projection sub-system projects the projected mask pattern from the mask onto the radiation sensitive wafer surface. Providing the sub-systems includes providing reflective lithography elements.

Providing reflective lithography elements includes providing reflective multilayer coated Ti doped high purity  $\text{SiO}_2$  glass lithography elements. Most preferably providing reflective lithography elements includes providing Ti doped high purity  $\text{SiO}_2$  glass projection substrates with defect free surfaces with figure and finish that are free of surface figure measurements  $> 0.25 \text{ nm rms}$ . Preferably the substrate surfaces have radiation manipulating surface shaped profiles, such as curved surfaces and shapes, preferably concave or convex which manipulate and operate on the radiation such that the mask pattern is projected and reduced. Preferably the substrate surfaces have

surface figures are less than 0.25 nm rms, have mid-spatial frequency roughness  $\leq$  0.20 nm rms, and high- spatial frequency roughness  $\leq$  0.10 nm rms.

In another aspect, the present invention includes a method of making a projection lithographic system and a method of projection lithography that includes providing an illumination sub-system which has an extreme ultraviolet soft x-ray source and providing a mask stage that has a reflective mask Ti doped high purity SiO<sub>2</sub> glass mask wafer with an unetched glass mask surface coated with a reflective multilayer coating. The method further includes providing a projection reduction sub-system including a plurality of Ti doped high purity SiO<sub>2</sub> glass reflective lithography elements, providing a wafer stage with a radiation sensitive semiconductor wafer; and aligning the illumination sub-system, the mask stage, the projection reduction sub-system, and the wafer stage wherein the extreme ultraviolet soft x-ray source illuminates the mask stage with extreme ultraviolet soft x-ray radiation and forms a circuit printing pattern which is projected, reduced, and focussed by the projection reduction sub-system onto said radiation sensitive wafer.

The invention further includes a method of making a reflective extreme ultraviolet soft x-ray lithography element which includes the steps of: providing a Ti doped high purity SiO<sub>2</sub> non-crystalline glass substrate free of inclusions and having a shaped element surface with surface figure features < 0.25 nm rms, mid-spatial frequency roughness < 0.20 nm rms, and high-spatial frequency roughness < 0.10 nm rms, and coating the shaped element glass surface with a reflective multilayer coating with uniform multilayer period thickness controlled to at least 0.1% rms to form a uniform reflective coating having a reflectivity of at least 65% to extreme ultraviolet soft x-rays.

The invention also includes a reflective extreme ultraviolet soft x-ray lithography element that comprises a Ti doped high purity SiO<sub>2</sub> inclusion-free glass having an unetched polished radiation manipulating shaped surface, with the surface free of printable surface figures.

The invention further includes a method of making a reflective extreme ultraviolet soft x-ray lithography element that has the steps of: providing a high purity Si containing feedstock and a high purity Ti containing feedstock, delivering the high purity Si containing feedstock and the high purity Ti containing feedstock to a

conversion site, converting the Si containing feedstock and the Ti containing feedstock into Ti doped SiO<sub>2</sub> soot, consolidating the Ti doped SiO<sub>2</sub> soot into an inclusion free homogeneous Ti doped high purity SiO<sub>2</sub> glass, forming the glass into a Ti doped high purity SiO<sub>2</sub> glass preform, forming the preform into a shaped surface lithography element substrate, measuring the figure and finish of the substrate surface, and coating the substrate surface with an extreme ultraviolet soft x-ray reflective coating.

Additional features and advantage of the invention will be set forth in the detailed description which follows, and in part will be readily apparent to those skilled in the art from that description or recognized by practicing the invention as described herein, including the detailed description which follows, the claims, as well as the appended Figures.

It is to be understood that both the foregoing general description and the following detailed description are merely exemplary of the invention, and are intended to provide an overview or framework for understanding the nature and character of the invention as it is claimed. The accompanying Figures are included to provide a further understanding of the invention, and are incorporated in and constitute a part of this specification. The Figures illustrate various embodiments of the invention, and together with the description serve to explain the principals and operation of the invention.

20

#### BRIEF DESCRIPTION OF THE DRAWINGS

FIG. 1 is a diagrammatic depiction of an embodiment of the invention;

FIG. 2 is a side view of an embodiment of the invention shows optical axis

25 alignment;

FIG. 3 is a side cross section of an embodiment of the invention;

FIG. 4 is a side cross section of an embodiment of the invention;

FIG. 5A-5C illustrate a lithographic pattern method of the invention;

FIG. 6 a diagrammatic depiction of an embodiment of the invention;

30 FIG. 7a-7c and 7A-7C are manufacturing flow depictions of embodiments of the invention;

FIG. 8 a plot of Thermal Expansion (y-axis) versus Temperature (x-axis) for various wt. % TiO<sub>2</sub> levels in SiO<sub>2</sub> glass;

FIG. 9 is a side view of an embodiment of the invention showing optical axis alignment;

5 and FIG. 10 is a side view of an embodiment of the invention showing optical axis alignment.

#### DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENTS

10 Reference will now be made in detail to the present preferred embodiments of the invention, examples of which are illustrated in the accompanying Figures. An exemplary embodiment of the projection lithographic method/system of the present invention is shown in FIG. 1 and is designated generally throughout by reference numeral 20.

15 In accordance with the invention, the present invention for a projection lithographic method includes providing a mask stage illuminated by extreme ultraviolet soft x-ray radiation  $\lambda$  produced by an illumination sub-system, with the mask stage including a mask for forming a projected mask pattern when illuminated by radiation  $\lambda$ . The invention includes providing Ti doped high purity SiO<sub>2</sub> glass lithography elements which manipulate radiation  $\lambda$  and operate on the projected mask pattern.

20 As embodied herein, and depicted in FIG. 1, projection lithographic method/system 20 comprises mask stage 22 which includes circuit patterned reflective mask 24. As shown in FIG. 1 Ti doped high purity SiO<sub>2</sub> glass lithography elements E reflect radiation  $\lambda$  utilizing reflective coated Ti doped high purity SiO<sub>2</sub> glass defect free surfaces 30. The reflective coated Ti doped high purity SiO<sub>2</sub> glass defect free surfaces include reflective multilayer coating 34 on glass surface 32, with reflective multilayer coating 34 preferably directly coating and bonding to Ti doped high purity SiO<sub>2</sub> glass surface 32. FIG. 2 shows the optical alignment of projection lithographic method/system 20 of FIG. 1. FIG. 3 shows a Ti doped high purity SiO<sub>2</sub> glass lithography element E, with reflective coated Ti doped high purity SiO<sub>2</sub> glass defect free surface 30 including reflective multilayer coating 34 on glass surface 32. Reflective lithography element E of FIG. 3 is preferably utilized with the invention in

the illumination sub-system. FIG. 4 shows a Ti doped high purity SiO<sub>2</sub> glass lithography element E with multilayer coating 34 on curved shaped glass surface 32. Reflective lithography element E of FIG. 4 is preferably utilized with the invention in the projection sub-system.

5       The projection lithographic method for producing integrated circuits with printed feature dimensions less than 100 nm includes providing illumination sub-system 36 for producing and directing extreme ultraviolet soft x-ray radiation  $\lambda$ . Illumination sub-system 36 includes extreme ultraviolet soft x-ray source 38. In a preferred embodiment, illumination sub-system 36 includes a laser produced plasma such as a 1.064  $\mu\text{m}$  neodymium YAG laser 40 which produces a Xenon gas plasma 42 from pulsed gas jet Xe clusters which outputs extreme ultraviolet soft x-ray radiation  $\lambda$  that is directed by condenser 44. Alternatively, extreme ultraviolet soft x-ray illumination sub-system 36 may comprise a synchrotron, discharge pumped x-ray lasers, an electron-beam driven radiation source device, or a radiation source based on high harmonic generation based on femto-second laser pulses. Preferably the illumination sub-system includes a plurality of Ti doped high purity lithography elements E which direct the source produced radiation  $\lambda$  and form condenser 44. Lithography elements E of condenser 44 form a funneling radiation  $\lambda$  pipe, preferably with the lithography elements having substantially flat reflecting surfaces, preferably situated to form glancing reflective angles with radiation  $\lambda$  emitting from source 38. Ti doped high purity SiO<sub>2</sub> lithography elements E may be utilized in a elliptical geometry to collect radiation  $\lambda$ .

10      The projection lithographic method includes providing mask stage 22 which is illuminated by the extreme ultraviolet soft x-ray radiation  $\lambda$  produced by illumination sub-system 36. Providing mask stage 22 includes providing a patterned reflective mask 24 which forms a projected mask pattern when illuminated by the radiation  $\lambda$ . Providing mask 24 preferably includes providing a Ti doped high purity SiO<sub>2</sub> glass wafer 26 which has a patterned absorbing overlay 28 overlaying reflective multilayer coated Ti doped high purity SiO<sub>2</sub> glass defect free wafer surface 3.

15      The method includes providing a projection sub-system 46 and an integrated circuit wafer 48 with a radiation sensitive wafer surface 50, wherein projection sub-

system 46 projects the projected mask pattern from mask 24 onto radiation sensitive wafer surface 50. Preferably projection sub-system 46 is a series of Ti doped high purity SiO<sub>2</sub> glass lithography elements E which reduce the size of the mask pattern and project the reduced pattern image onto wafer surface 50. A preferred embodiment is shown in FIG. 1-2 having four glass lithography elements E.

In a preferred embodiment, extreme ultraviolet soft x-ray radiation  $\lambda$  is in the range from about 5 nm to about 15 nm, and most preferably illumination sub-system 36 directs radiation centered about 13.4 nm to reflective mask 24, with the Ti doped high purity SiO<sub>2</sub> glass lithography elements E having a reflectivity of at least 65% at 13.4 nm. With extreme ultraviolet soft x-ray radiation  $\lambda$  centered about 13.4 nm, Ti doped high purity SiO<sub>2</sub> glass surfaces 32 are coated with reflective multilayer coating 34 comprising alternating layers of Mo and Si. Alternatively it is preferred that extreme ultraviolet soft x-ray radiation  $\lambda$  is centered about 11.3 nm, with Ti doped high purity SiO<sub>2</sub> glass surfaces 32 coated with reflective multilayer coating 34 comprising alternating layers of Mo and Be. With  $\lambda$  centered about 11.3 nm, reflective Ti doped high purity SiO<sub>2</sub> glass lithography elements E have a reflectivity of at least 70% at 11.3 nm. Multilayer coating 34 is preferably comprised of alternating layers of a first element and a second element wherein the coating is highly reflective to the extreme ultraviolet soft x-ray radiation  $\lambda$ .

The provided Ti doped high purity SiO<sub>2</sub> glass of lithography elements E is defect free in that the glass body is free of bulk glass defects and is free of inclusion, including glass voids and gas filled voids in the glass, and is particularly free of any such defects or imperfections or structures that have a dimension greater than 80 nm. In a particularly preferred embodiment, glass surface 32 is an unetched glass surface that has been finished such as with polishing, to a surface free of surface figure measurement > 0.25 nm rms, and preferably finished to at least .15 nm rms roughness. The Ti doped SiO<sub>2</sub> glass is substantially non-transmissive to extreme ultraviolet soft x-ray radiation  $\lambda$  and the reflectivity of reflective coated wafer surface 30 and the ultra-low roughness of defect free surface 32 is utilized in the invention to inhibit scattering of radiation  $\lambda$  and provide an ultra-stable high quality image during the projection lithography process. The provided Ti doped high purity SiO<sub>2</sub> glass is highly stable,

homogeneous, chemical attack resistant, is preferably chlorine-free and preferably has an impurity level of less than 10 ppb of alkali and alkaline earth metals. Preferably the Ti doped high purity SiO<sub>2</sub> glass contains from 5 to 10 wt. % TiO<sub>2</sub> and has a coefficient of thermal expansion in the range from +30 ppb to -30 ppb at 20° C., and more

- 5 preferably in the range from +10 ppb to -10 ppb at 20° C. Preferably the TiO<sub>2</sub> content is from 6 to 8 wt. %, more preferably from 6.5 to 7.5 wt. %, and most preferably about 7 wt. %. Preferably lithography elements E have a variation in coefficient of thermal expansion throughout the glass substrate body that is less than or equal to 15 ppb. The lithography element glass preferably has a thermal conductivity  $K \leq 1.40 \text{ w/(m.}^{\circ}\text{C.)}$  at 10 25° C., more preferably in the range from 1.25 to 1.38, and most preferably about 1.31. During projection lithography, lithography elements E and mask 24 are heated by the illumination of radiation  $\lambda$ , and even with such irradiation and heating of lithography elements E changes of the patterned circuit projected image are inhibited and the quality of the projected image is maintained. In the inventive method, the Ti doped 15 high purity SiO<sub>2</sub> glass is heated to an operating temperature by radiation  $\lambda$  and preferably the glass has an adjusted Ti dopant level adjusted such that the glass has a coefficient of thermal expansion centered about 0 at such operating temperature, preferably with the zero thermal expansion range also including the lithography element manufacturing temperatures and the lithography element non-operating rest 20 temperature. With such a thermal conductivity and coefficient of thermal expansion, the Ti doped high purity SiO<sub>2</sub> glass provides appropriate lithographic operation and stability, and provides a highly reliable and economic lithographic method/system in which the lithography elements do not require cooling in addition to economically 25 manufacturable elements. In a preferred embodiment, the lithography elements are not actively cooled and are free of a cooling system such as circulating cooling fluids, thermal electric coolers, or other means for removing heat build up during operation of the lithography system.

- The provided Ti doped high purity SiO<sub>2</sub> glass defect free surface 32 are free of surface figure measurements  $> 0.25 \text{ nm rms}$ . Finishing methods including chemical 30 mechanical polishing are beneficially accepted by the non-crystalline glass. With shaped surface mirror lithography elements, an appropriately shaped and sized curve is generated in the non-crystalline glass body substrate and finished. The defect free glass

surface of lithography elements E preferably have a surface figure no greater than 0.25 nm rms, and a high-spatial frequency roughness (at frequencies above  $1 \mu\text{m}^{-1}$ ) no greater than 0.10 nm rms. The reflective multilayer coated Ti doped high purity SiO<sub>2</sub> glass surface 30 preferably reflects at least 65%, and more preferably reflects at least 70%, of the radiation  $\lambda$  illuminating the reflective multilayer coating. In a preferred embodiment surface 32 is unetched and free of an intermediate barrier layer or release layer so that multilayer reflective coating 34 is directly bonded thereto.

The invention further comprises a method of making a projection lithographic system and the method of projecting lithographic patterns that includes providing an illumination sub-system 36 with an extreme ultraviolet soft x-ray source 38 and providing a mask stage 22 with a mask receiving member 52 and a reflective mask Ti doped high purity SiO<sub>2</sub> glass mask wafer 24 with an unetched glass mask wafer surface coated with a reflective multilayer coating having a reflectivity of at least 65% to extreme ultraviolet soft x-rays received in mask receiving member 52. The method further includes providing a projection reduction sub-system 46 with a plurality of Ti doped high purity SiO<sub>2</sub> glass lithography elements E; providing wafer stage 56 with a radiation sensitive semi-conductor wafer 58; and aligning illumination sub-system 36, mask stage 22, projection sub-system 46, and wafer stage 56 such that extreme ultraviolet soft x-ray source 38 illuminates reflective mask 24 with extreme ultraviolet soft x-ray radiation and reflective mask 24 reflects the radiation and forms a printing pattern of which is projected, reduced and focused onto radiation sensitive print media 58. The method includes providing a reflective mask 24 with a Ti doped high purity SiO<sub>2</sub> glass mask wafer 26 free of inclusions and wafer surface defects that are printable on radiation sensitive print media 58.

The method preferably includes determining operating temperatures of lithography elements when illuminated by radiation  $\lambda$  during operation of system 20 and providing a lithography elements E includes providing Ti doped high purity SiO<sub>2</sub> glass having a coefficient of thermal expansion at the determined operating temperature centered about 0. Such tuning of the coefficient of thermal expansion is done by controlled tuning of Ti dopant content in the SiO<sub>2</sub> glass as demonstrated by FIG. 8 which shows the thermal expansion characteristics for high purity SiO<sub>2</sub> glasses with TiO<sub>2</sub> wt. % from 0 to 9.0 wt. % TiO<sub>2</sub>.

In a further embodiment of the method the Ti doped high purity SiO<sub>2</sub> glass is heated to a raised temperature range by the extreme ultraviolet soft x-ray radiation and the Ti doped high purity SiO<sub>2</sub> glass has a coefficient of thermal expansion for the raised temperature range that is less than 10 ppb and greater than -10 ppb. Preferably the  
5 method includes providing a Ti doped high purity SiO<sub>2</sub> glass that is a thermal insulator (low thermal conductivity) with a thermal conductivity K ≤ 1.40 w/(m.° C.), preferably in the range from 1.25 to 1.38, most preferably about 1.31, and allowing the glass to heat up without actively cooling the glass and reflective elements E.

The invention further comprises a method of making a reflective extreme  
10 ultraviolet soft x-ray projection lithography element that includes providing a Ti doped high purity SiO<sub>2</sub> non-crystalline substrate free of inclusions and having a shaped element surface with surface figure features < 0.25 nm rms, mid-spatial frequency roughness < 0.20 nm rms, and high-spatial frequency roughness < 0.10 nm rms, and coating the element glass surface with a reflective multilayer coating 34 with a uniform multilayer period thickness controlled to at least 0.1 % rms to form a uniform reflective  
15 surface 30 having a reflectivity of at least 65% to extreme ultraviolet soft x-rays. The method further includes generating a curved surface 30. Coating with a reflective multilayer coating preferably includes forming alternating smooth thin layers ( $\leq 4$  nm thick) of a first element and a second element, such as Mo/Si or Mo/Be.

20 The alternating layers provide a soft x-ray extreme ultraviolet reflectivity peak, preferably centered about 13.4 nm and alternatively centered about 11.3 nm. Such an alternating multilayer reflective coating functions similarly to a quarter wave stack, with the thickness of the layers optimized for constructive interference of the photons reflected at each interface and minimal absorption so a large number of interfaces  
25 contribute to the high reflectance of the coating. Preferably layer to layer variation in thickness is within 0.01 nm with the multilayer coating 34 controlled in thickness to about 0.1 % rms across lithography element surface 32, preferably a uniformity better than 0.01 % so that surface shape 32 is maintained. In a preferred embodiment, the coating is alternating layers of Mo and Si with Mo thickness about 2.8 nm and the Si thickness about 4.0 nm. With proper deposition conditions, reflectances of 68% or  
30 greater at 13.4 nm are achievable with such alternating layers. The alternating layers of

Mo and Si are preferably capped with a capping layer to prevent oxidation of the Mo on exposure to normal atmospheres, with a preferred capping layer being a Si layer.

In the preferred embodiment, the reflective multilayer coating is directly deposited onto the Ti doped high purity  $\text{SiO}_2$  glass surface. The finished surface and the Ti doped high purity  $\text{SiO}_2$  glass properties provide for directly depositing and bonding between the glass surface and the reflective multilayer coating without the need for additional treatments of a substrate surface such as smoothing coating processes used with glass-ceramic crystalline containing substrate materials and remove the need for intermediate layers between the glass surface and the reflective multilayer coating. A superior and stable reflective lithography element is achieved with the direct bonding contact between the finished Ti doped high purity  $\text{SiO}_2$  glass surface and the multilayer reflective coating.

Preferably providing a Ti doped high purity  $\text{SiO}_2$  glass preform as shown in FIG. 6, includes providing a high purity Si containing feedstock 114 and a high purity Ti containing feedstock 126, delivering high purity Si containing feedstock 114 and high purity Ti containing feedstock 126 to a conversion site 100, converting the delivered feedstocks into Ti doped  $\text{SiO}_2$  soot 101, depositing soot 101 in a revolving zircon collection cup 142 of refractory zircon furnace 140 onto the upper glass surface of hot Ti doped high purity  $\text{SiO}_2$  glass body 144, concurrently with the soot deposition consolidating the Ti doped  $\text{SiO}_2$  soot into an inclusion free homogeneous Ti doped high purity  $\text{SiO}_2$  glass body 144 and forming glass body 144 into a Ti doped high purity  $\text{SiO}_2$  glass preform. Preferably providing high purity Si containing feedstock 114 and high purity Ti containing feedstock 126 includes providing a chlorine-free high purity Si containing feedstock and providing a chlorine-free high purity Ti containing feedstock, 20 consolidating the chlorine-free feedstocks into a chlorine-free Ti doped  $\text{SiO}_2$  soot and consolidating the soot into a chlorine-free Ti doped  $\text{SiO}_2$  glass. Preferably the Si feedstock is a siloxane, preferably a polymethylsiloxane, more preferably a polymethylcyclosiloxane, and most preferably high purity octamethylcyclotetrasiloxane (Si feedstock comprised of at least 99% octamethylcyclotetrasiloxane). Preferably the 25 Ti feedstock is a titanium alkoxide, and more preferably titanium isopropoxide [Ti(OPri)<sub>4</sub>], preferably with Ti feedstock comprised of at least 99% titanium isopropoxide. A nitrogen inert carrier gas 116 is bubbled through feedstocks 114 and

126, and a nitrogen inert carrier gas 118 is added to the Si feedstock vapor/carrier gas mixture and the Ti feedstock vapor/carrier gas mixture to prevent saturation and facilitate delivery of the feedstocks to conversion site 100, through distribution systems 120 and manifold 122. Preferably the Si feedstock is mixed with the Ti feedstock in

5 manifold 122 to form a homogeneous gaseous Ti doped SiO<sub>2</sub> glass precursor mixture which is delivered through conduits 134 to conversion site burners 136 mounted in the upper portion 138 of furnace 140 which produce conversion site burner flames 137, so that the feedstock mixture is converted into Ti doped SiO<sub>2</sub> soot 101 and then homogeneous Ti doped SiO<sub>2</sub> glass 144. The weight percent dopant content of TiO<sub>2</sub> in

10 the SiO<sub>2</sub> glass can be adjusted by changing the amount of Ti feedstock delivered to conversion site 100 that is incorporated into soot 101 and glass 144. In a preferred method the Ti dopant weight percent level of glass 144 and substrate therefrom is adjusted so that the substrate has a coefficient of thermal expansion centered about 0 at an operating temperature of the reflective lithography elements. In accordance with

15 FIG. 8, adjustments to the wt. % TiO<sub>2</sub> in the high purity SiO<sub>2</sub> glass adjust the thermal expansion characteristics of the resultant glass. Preferably the Ti dopant weight percent level of the glass is adjusted within the range from about 6 wt. % TiO<sub>2</sub> to about 9 wt. % TiO<sub>2</sub>, most preferably in the range from 7 to 8 wt. %. Conversion site burner flames 137 are formed with a fuel/oxygen mixture (Natural Gas and/or H<sub>2</sub>/with O<sub>2</sub>) which

20 combusts, oxidizes and converts the feedstocks at temperatures  $\geq$  about 1600° C. into soot 101 and consolidates the soot into glass 144. The temperatures of conduits 134 and the feedstocks contained therein are preferably controlled and monitored to inhibit reactions prior to flames 137 which may disrupt the flow of the feedstocks and soot 101 and complicate the manufacturing process of glass 144. Preferably furnace 140 and

25 particularly zircon cup 142 and upper portion 138 are made from high purity zircon refractory bricks which are free of alkali and alkaline earth metals, and other impurities which tend to migrate from the furnace and contaminate glass 144. Such high purity bricks can be obtained using high purity ingredients and calcining the bricks to leach out impurities.

30 Preferably cup 142 has a circular diameter shape of at least 0.5 meters, more preferably at least 1 meter, most preferably a diameter of about 1.5 meters so that glass body 144 is a cylindrical boule having a respective diameter of at least 0.5 meters,

preferably 1 meter, most preferably 1.5 meters, and a height of at least 8 cm, preferably at least 10 cm, preferably about 15 cm thick, with a preferred height in the range from 12 to 16 cm. Forming glass body 144 into a Ti doped high purity  $\text{SiO}_2$  glass substrate, preferably includes inspecting glass body 144 for any glass defects, such as inclusions, 5 if defect found selecting defect free sections of glass body 144 such as defect free section 62 of glass body 144 shown in FIG. 7A, and removing the defect free section 62 from 144 as shown in FIG. 7B. Substrates are then formed from removed defect free section 62 as shown in FIG. 7C. Preform 60 is preferably obtained by core drilling section 62 from body 144, and then a curve is generated and finished to provide 10 lithography element E in FIG. 7C.

FIG. 7a-7c show a method for making large lithography elements, preferably greater than one meter in the lithography elements largest dimension, which utilizes a majority of boule glass disk 144 formed as shown in FIG. 6. Such large reflective lithography elements are preferred in terms of minimizing diffraction effects of radiation  $\lambda$  and provides an improved projected image on the integrated circuit wafer with excellent resolution. Improved projected images are provided by the method, system, and lithography elements of the invention with minimizing detrimental diffraction effects by maximizing the size of the reflective lithography elements utilizing Ti doped high purity  $\text{SiO}_2$  glass. Preferably to maximize the size of the largest lithography 15 element in the system a glass disk 144 is formed that has a diameter  $D \geq 1\text{m}$ , more preferably  $D \geq 1.5\text{ m}$ .

The invention further includes reflective extreme ultraviolet soft x-ray lithography elements. The lithography element E comprises a Ti doped high purity  $\text{SiO}_2$  inclusion-free glass substrate that preferably is unetched. FIGS. 1-4 show such Ti 20 doped high purity  $\text{SiO}_2$  inclusion-free glass lithography elements. FIGS. 5A-5C show a lithographic integrated circuit printing pattern IC. As shown in FIG. 5A lithographic integrated circuit printing pattern IC is on reflective mask 24. Projected lithographic integrated circuit printing pattern IC of FIG. 5B is formed by illuminating the mask of FIG. 5A with extreme ultraviolet soft x-ray radiation  $\lambda$ . Lithographic integrated circuit 25 printing pattern IC of FIG. 5C is formed on wafer surface 50 of wafer 48 from projected pattern IC of FIG. 5B which was produced from the mask pattern IC of FIG. 5A.

The invention also includes a method of making a reflective extreme ultraviolet soft x-ray lithography element that comprises providing a Ti doped high purity  $\text{SiO}_2$  glass preform 60 free of glass inclusions. Preferably providing a Ti doped high purity  $\text{SiO}_2$  glass preform includes providing a high purity Si containing feedstock 114 and high purity Ti containing feedstock 126, delivering feedstocks 114 and 126 to conversion site 100, converting feedstocks 114 and 126 into Ti doped  $\text{SiO}_2$  soot 101, consolidating soot 101 into an inclusion free homogeneous Ti doped high purity  $\text{SiO}_2$  glass and forming the glass into a Ti doped high purity  $\text{SiO}_2$  glass preform 60.

Preferably Si feedstock 114 is chlorine free and Ti feedstock 126 is chlorine free so that soot 101 and glass 144 is chlorine free.

As shown in FIG. 6, cup 142 has a diameter D of at least a half meter, preferably at least about one meter, more preferably a diameter D of at least a one and a half meter, preferably a height depth thickness H of at least 12cm, preferably at least 20 cm, with glass 144 conforming to these dimensions and shape of cup 142 so that glass body 144 has a diameter D and a depth H, with  $D > H$ , preferably with the glass body from which the Ti doped high purity  $\text{SiO}_2$  glass preform is made is a disk with  $D \geq 2H$ , preferably  $D \geq 3H$ , more preferably  $D \geq 4H$ , and most preferably  $D \geq 5H$ .

In a preferred embodiment such as in FIG. 7A-7C, the preform is formed from the glass body with a largest dimension X, such that diameter D of the glass body is greater than  $2X$ , preferably greater than  $3X$ .

FIG. 9 shows an embodiment of the invention with four reflective lithographic elements E, with the projection sub-system including mirror M1, mirror M2, mirror M3, and mirror M4. FIG. 10 shows an embodiment of the invention with three reflective lithographic elements E, with the projection sub-system including mirror M1, mirror M2, and mirror M3.

The finishing steps in addition to polishing the preform surfaces includes cleaning the preform surfaces to remove finishing products to provide a clean smooth surface for contact with the reflective multilayer coating. Preferably the step of providing a preform includes adjusting the Ti dopant weight percent level of the Ti doped high purity  $\text{SiO}_2$  glass preform so that the glass has a coefficient of thermal expansion centered about 0 at an operating temperature of the reflective extreme ultraviolet soft x-ray lithography element.

The inventive method of making a reflective extreme ultraviolet soft x-ray lithography element provides an economic means for efficiently manufacturing large quantities of glass substrate bodies which can enable the utilization of extreme ultraviolet soft x-ray projection lithography for the mass production volume manufacturing of integrated circuits with printed feature dimensions less than 100 nm. Additionally, the inventive method of making a reflective lithography element Ti doped high purity SiO<sub>2</sub> glass includes the beneficial steps of inspecting and qualifying the finished glass surface 32 to ensure that the surface has proper figure and finish and is defect free, and additionally inspecting and qualifying the CTE of the glass.

- 5 During use of the reflective extreme ultraviolet soft x-ray lithography elements E, the Ti doped SiO<sub>2</sub> glass substrate has a lithography operating temperature. The operating temperature of the high purity Ti doped SiO<sub>2</sub> glass element include a maximum operating temperature. During the making of the reflective extreme ultraviolet soft x-ray lithography element, the high purity Ti doped SiO<sub>2</sub> glass is exposed to manufacturing treatment temperatures. The manufacturing treatment temperatures include elevated temperatures during cutting, machining, finishing, and coating. The manufacturing treatment temperatures include a maximum manufacturing temperature. Preferably the high purity Ti doped SiO<sub>2</sub> glass is crystallization resistant and has a crystallization temperature T<sub>crystal</sub> at which crystallization in the glass is induced, with T<sub>crystal</sub> being substantially greater than the maximum operating temperature and the maximum manufacturing temperature. Preferably T<sub>crystal</sub> is at least 400° C. higher than the greater of the maximum operating temperature and the maximum manufacturing temperature, more preferably at least 700° C. higher, and most preferably at least 800° C higher. Preferably T<sub>crystal</sub> is ≥ 1300° C., with the maximum operating temperature and maximum manufacturing temperature not exceeding 500° C. With the high purity Ti doped SiO<sub>2</sub> glass, the crystallization of the glass is inhibited at the elevated temperatures experienced during manufacturing and use of the lithography elements E. The glass is thus beneficial in that it has a high temperature crystallization behavior.
- 10 20 25 30 Additionally, in view of the manufacturing treatment temperatures and the lithography operating temperatures, the high purity Ti doped SiO<sub>2</sub> glass maintains its physical dimensions when exposed to thermal cycling. In the use and manufacture of

the high purity Ti doped SiO<sub>2</sub> glass lithography element repeated thermal cycling between lower temperatures and higher temperatures do not substantially change the physical dimensions of the glass substrate. Preferably the high purity Ti doped SiO<sub>2</sub> glass is resistant to thermal cycling hysteresis, and most preferably is free of thermal cycling hysteresis when repeatedly cycled (> 100 cycles) from the lowest lithography operating temperature to the highest lithography operating temperature. Most preferably physical dimensions of the glass substrate and lithography element do not measurably change when repeatedly cycled from a low temperature to a high temperature that are below 300° C. with the low temperature proximate to 0° C. and the high temperature proximate to 300° C.

In a preferred embodiment, the high purity Ti doped SiO<sub>2</sub> glass lithography element has a birefringence resulting from permanent strain in the glass of less than 10 nm/cm, and more preferably is less than 2 nm/cm. Preferably birefringence less than 2 nm/cm is achieved by providing a constant homogeneous distribution of Ti dopant throughout the SiO<sub>2</sub> glass with the coefficient of thermal expansion in the range from + 10 ppb to - 10 ppb at 20° C., with the maximum fluctuation in the coefficient of thermal expansion being less than 10 ppb, most preferably less than 5 ppb. Additionally, low birefringence may be obtained by annealing the high purity Ti doped SiO<sub>2</sub> glass. Preferably the high purity Ti doped SiO<sub>2</sub> glass lithography element substrate is annealed at a temperature of at least 900° C more preferably at least 1000°C, and most preferably at least 1030°C, and preferably at a time after the glass has experienced stress from machining.

Ensuring such low birefringence levels is achieved by monitoring the coefficient of thermal expansion of the high purity Ti doped SiO<sub>2</sub> glass by transmitting ultrasonic waves through the glass and measuring the transit time of the ultrasonic waves through the glass to determine the ultrasonic velocity and expansivity characteristics of the glass exposed to the ultrasonic waves. Preferably such measurement and monitoring of the high purity Ti doped SiO<sub>2</sub> glass is utilized throughout the glass lithography element manufacturing process. Such ultrasonic measurements are preferably utilized for quality control, inspection, and selection. Such measurement are preferably utilized to insure the manufacturing of glass body 144 is providing a constant homogeneous distribution of Ti dopant. Additionally, such measurements are used in inspecting glass

body 144 and selecting sections 62 to be cut from glass body 144. Further such measurements may be utilized to insure that undue stresses are not formed in the glass during later manufacturing stages such as finishing and also used as a determining factor if further annealing of the glass is required.

5        A preform 60 may be obtained by cutting the preform from an inclusion free area of a Ti doped high purity SiO<sub>2</sub> glass boule disk 144 that had a cylindrical disk shape of approximately 152 cm diameter and about a 20 cm thickness (height). The boule is preferably produced from octamethylcyclotetrasiloxane and titanium isopropoxide feedstocks in accordance with FIG. 6 of the invention with the high purity  
10      Ti doped high purity SiO<sub>2</sub> glass having a TiO<sub>2</sub> wt. % of about 7.5 wt. %.

It will be apparent to those skilled in the art that various modifications and variations can be made to the present invention without departing from the spirit and scope of the invention. Thus, it is intended that the present invention cover the modifications and variations of this invention provided they come within the scope of the appended claims and their equivalents.  
15

WE CLAIM:

1. A projection lithographic method for producing integrated circuits with printed feature dimensions less than 100 nm, said method comprising:

5 providing an illumination sub-system for producing and directing an extreme ultraviolet soft x-ray radiation  $\lambda$ , said illumination sub-system including an extreme ultraviolet soft x-ray source, providing a mask stage and a mask illuminated by said extreme ultraviolet soft x-ray radiation  $\lambda$  produced by said illumination sub-system for forming a projected lithographic pattern when illuminated by said radiation  $\lambda$ , providing

10 a projection sub-system, providing a wafer stage and an integrated circuit wafer, said integrated circuit wafer having a  $\lambda$  radiation sensitive wafer surface, projecting with said projection sub-system the projected lithographic pattern from said mask onto said radiation sensitive wafer surface, said sub-systems comprising a plurality of reflective lithography elements, said lithography elements including a plurality of reflective multilayer coated Ti doped high purity  $\text{SiO}_2$  glass substrates with defect free glass

15 surfaces for manipulating said radiation and said lithographic pattern.

2. A method as claimed in claim 1 wherein said Ti doped  $\text{SiO}_2$  glass is inclusion free.

20

3. A method as claimed in claim 1 wherein said Ti doped  $\text{SiO}_2$  glass substrate surface is an unetched glass surface.

25

4. A method as claimed in claim 1 wherein said Ti doped  $\text{SiO}_2$  glass is substantially non-transmissive to said extreme ultraviolet soft x-ray radiation  $\lambda$ .

5. A method as claimed in claim 1 wherein said Ti doped  $\text{SiO}_2$  glass substrate contains from 5 to 10 wt. %  $\text{TiO}_2$  and has a coefficient of thermal expansion in the range from + 30 ppb to - 30 ppb at 20° C.

30

6. A method as claimed in claim 5 wherein said coefficient is in the range of + 10 ppb to - 10 ppb at 20° C.

7. A method as claimed in claim 5 wherein said substrate has a variation in coefficient of thermal expansion  $\leq 15$  ppb.

5 8. A method as claimed in claim 6 wherein said substrate has a variation in coefficient of thermal expansion  $\leq 15$  ppb.

9. A method as claimed in claim 1 wherein said substrate has a thermal conductivity  $K \leq 1.40$  w/(m x °C) at 25°C.

10 10. A method as claimed in claim 1 wherein said Ti doped SiO<sub>2</sub> glass substrate is heated by said radiation  $\lambda$ , and said projected lithographic pattern on said integrated circuit wafer is substantially unaffected by said heating of said glass substrate.

15 11. A method as claimed in claim 5, wherein said Ti doped SiO<sub>2</sub> glass is heated to an operating temperature by said radiation  $\lambda$  and said Ti doped SiO<sub>2</sub> glass has a Ti dopant level such that said glass has a coefficient of thermal expansion at said operating temperature centered about 0.

20 12. A method as claimed in claim 1 wherein said reflective multilayer coated Ti doped SiO<sub>2</sub> glass lithography element surface reflects at least 65% of the radiation  $\lambda$  illuminating the reflective lithography element.

25 13. A method as claimed in claim 12 wherein said reflective multilayer coated Ti doped SiO<sub>2</sub> glass surface reflects at least 70% of the radiation  $\lambda$ .

14. A method as claimed in claim 12 wherein said reflective multilayer is directly coated on said Ti doped SiO<sub>2</sub> glass surface.

30 15. A method as claimed in claim 1 wherein said reflective lithography elements have a maximum operating temperature and a maximum manufacturing temperature and said Ti doped high purity SiO<sub>2</sub> glass has a crystallization temperature  $T_{\text{crystal}}$  which

is greater than said maximum operating temperature and said maximum manufacturing temperature.

16. A method as claimed in claim 15 wherein  $T_{\text{crystal}}$  is at least 400°C higher than

5 said maximum operating temperature and said maximum manufacturing temperature.

17. A method as claimed in claim 16 wherein  $T_{\text{crystal}} > 1100^{\circ}\text{C}$

18. A method as claimed in claim 1 wherein said reflective lithography elements

10 have a lowest lithography operating temperature and a highest lithography operating temperature, said Ti doped high purity SiO<sub>2</sub> glass free of thermal cycling hysteresis when repeatedly cycled at least a hundred times from said lowest lithography operating temperature to said highest lithography operating temperature.

15 19. A method as claimed in claim 1 wherein said Ti doped high purity SiO<sub>2</sub> glass

has a birefringence less than 10 nm/cm.

20. A method of making a projection lithographic printing pattern, said method comprising:

20 providing an illumination sub-system including an extreme ultraviolet soft x-ray source, providing a mask stage, said mask stage including a reflective mask Ti doped high purity SiO<sub>2</sub> glass mask wafer with a glass mask wafer surface coated with a reflective multilayer coating providing a projection reduction sub-system, said

projection reduction sub-system including a plurality of Ti doped high purity SiO<sub>2</sub> glass

25 reflective lithography elements, providing a wafer stage, said wafer stage including a radiation sensitive semiconductor wafer, aligning said illumination sub-system, said

mask stage, said projection reduction sub-system, and said wafer stage wherein said

extreme ultraviolet soft x-ray source illuminates said mask with extreme ultraviolet soft x-ray radiation, said reflective mask reflects said radiation and forms a printing pattern

30 which is reduced and projected by said projection reduction sub-system Ti doped high

purity SiO<sub>2</sub> glass reflective lithography elements onto said semiconductor wafer.

21. A method as claimed in claim 20, wherein providing said illumination subsystem includes providing a plurality of Ti doped high purity SiO<sub>2</sub> glass reflective lithography elements which direct and condense said extreme ultraviolet soft x-ray radiation.

5

22. A method as claimed in claim 20, wherein providing Ti doped high purity SiO<sub>2</sub> glass reflective lithography elements includes providing shaped glass surfaces free of surface figure measurements >0.25 nm rms.

10

23. A method as claimed in claim 21, wherein providing Ti doped high purity SiO<sub>2</sub> glass reflective lithography elements includes providing glass surfaces free of surface figure measurements >0.25 nm rms.

15

24. A method as claimed in claim 20, further comprising, determining an operating temperature of said reflective lithography elements when exposed to said extreme ultraviolet soft x-ray radiation during operation of said method, and providing a reflective lithography elements includes providing lithography elements with Ti doped high purity SiO<sub>2</sub> glass having coefficients of thermal expansion at the operating temperatures of the reflective lithography elements that are centered about 0.

20

25. A method as claimed in claim 20, wherein said Ti doped high purity SiO<sub>2</sub> glass is heated to a raised temperature range by said extreme ultraviolet soft x-ray radiation and said Ti doped high purity SiO<sub>2</sub> glass has a coefficient of thermal expansion for said raised temperature range that is less than 10 ppb and greater than -10 ppb.

25

26. A method as claimed in claim 25 wherein said glass has a variation in coefficient of thermal expansion ≤ 10 ppb

30

27. A method as claimed in claim 20 wherein said Ti doped high purity SiO<sub>2</sub> glass has a thermal conductivity K ≤ 1.40 w/(m x °C) at 25°C.

28. A method of making a reflective extreme ultraviolet soft x-ray lithography element said method comprising:

providing a Ti doped high purity SiO<sub>2</sub> glass substrate free of inclusions and having a shaped element glass surface with surface figure features <0.25 nm rms, mid-spatial frequency roughness < 0.20 nm rms, and high spatial frequency roughness < 0.10 rms,

coating said shaped element glass surface with a reflective multilayer coating with uniform multilayer period thickness controlled to at least 0.1% rms, to form a uniform reflective coating having a reflectivity of at least 65% to extreme ultraviolet soft x-rays.

10

29. A method as claimed in claim 28, wherein said lithography element is heated to a raised temperature range by extreme ultraviolet soft x-ray radiation and said Ti doped high purity SiO<sub>2</sub> glass has a coefficient of thermal expansion for said raised temperature range that is less than 10 ppb and greater than -10 ppb and has a variation ≤ 10 ppb.

15

30. A method as claimed in claim 28, wherein providing a Ti doped high purity SiO<sub>2</sub> glass substrate further includes, providing a high purity Si containing feedstock and a high purity Ti containing feedstock, delivering said high purity Si containing feedstock and said high purity Ti containing feedstock to a conversion site, converting said Si containing feedstock and said Ti containing feedstock into Ti doped SiO<sub>2</sub> soot, consolidating said Ti doped SiO<sub>2</sub> soot into an inclusion free homogeneous Ti doped high purity SiO<sub>2</sub> glass, forming said glass into a Ti doped high purity SiO<sub>2</sub> glass substrate.

20

25

31. A method as claimed in claim 30, wherein providing a high purity Si containing feedstock and a high purity Ti containing feedstock includes providing a chlorine-free high purity Si containing feedstock and providing a chlorine-free high purity Ti containing feedstock, converting said chlorine-free feedstocks into chlorine-free Ti doped SiO<sub>2</sub> soot, and consolidating said soot into a chlorine-free Ti doped SiO<sub>2</sub> glass.

30

32. A method as claimed in claim 28, wherein said glass substrate has a Ti dopant weight percent level and providing a Ti doped high purity SiO<sub>2</sub> glass substrate further includes adjusting said Ti dopant weight percent level so that said glass substrate has a coefficient of thermal expansion centered about 0 at an operating temperature of said mask.

5

33. A reflective extreme ultraviolet soft x-ray lithography element comprising:  
a Ti doped high purity SiO<sub>2</sub> inclusion-free glass having an unetched polished radiation manipulating shaped surface with the surface free of printable surface figure defects that have a surface figure measurement > 0.25 nm rms, said surface having a mid-spatial frequency roughness < 0.20 nm rms, and a high-spatial frequency roughness < 0.10 nm rms.

10

34. A lithography element as claimed in claim 33 wherein said glass consists of silicon, titanium, and oxygen.

15

35. A lithography element as claimed in claim 33 wherein said glass contains from 5 to 10 wt. % TiO<sub>2</sub> and has a coefficient of thermal expansion in the range from +10 ppb to -10 ppb at 20°C.

20

36. A lithography element as claimed in claim 33 wherein said glass is crystal-free.

25

37. A lithography element as claimed in claim 33 wherein said glass is free of chlorine.

30

38. A lithography element as claimed in claim 33 wherein said glass has an impurity level of less than 10 ppm of alkali and alkaline earth metals and said Ti dopant is homogeneously distributed in said glass.

35

39. A method of making a reflective extreme ultraviolet soft x-ray lithography element said method comprising:

providing a high purity Si containing feedstock and a high purity Ti containing feedstock, delivering said high purity Si containing feedstock and said high purity Ti containing feedstock to a conversion site, converting said Si containing feedstock and said Ti containing feedstock into Ti doped SiO<sub>2</sub> soot, consolidating said Ti doped SiO<sub>2</sub> soot into an inclusion free homogeneous Ti doped high purity SiO<sub>2</sub> glass, forming said glass into a Ti doped high purity SiO<sub>2</sub> glass preform.

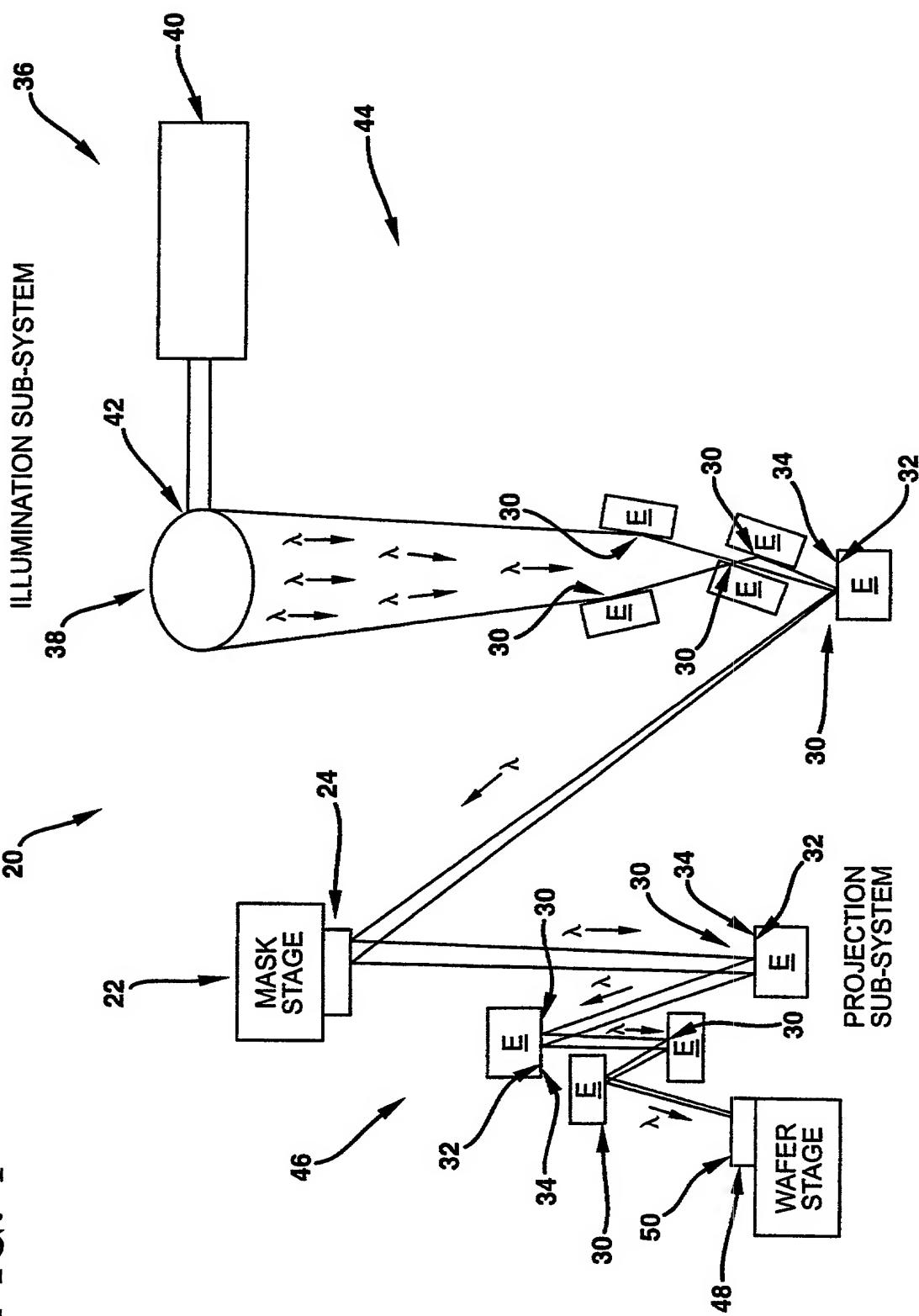
5           40. A method as claimed in claim 39 further comprising forming said preform into a shaped surface lithography element substrate, measuring a figure and finish of said  
10          substrate surface, and coating the substrate surface with an extreme ultraviolet soft x-ray reflective coating.

15          41. A method as claimed in claim 39, wherein providing a high purity Si containing feedstock includes providing a chlorine-free high purity Si containing feedstock and providing a chlorine-free high purity Ti containing feedstock, converting said chlorine-free feedstocks into chlorine-free Ti doped SiO<sub>2</sub> soot, and consolidating said soot into chlorine-free Ti doped SiO<sub>2</sub> glass.

20          42. A method as claimed in claim 39, wherein said glass preform has a Ti dopant weight percent level and providing a Ti doped SiO<sub>2</sub> glass preform further includes adjusting said Ti dopant weight percent level so that said preform has a coefficient of thermal expansion centered about 0 at an operating temperature of said lithography element.

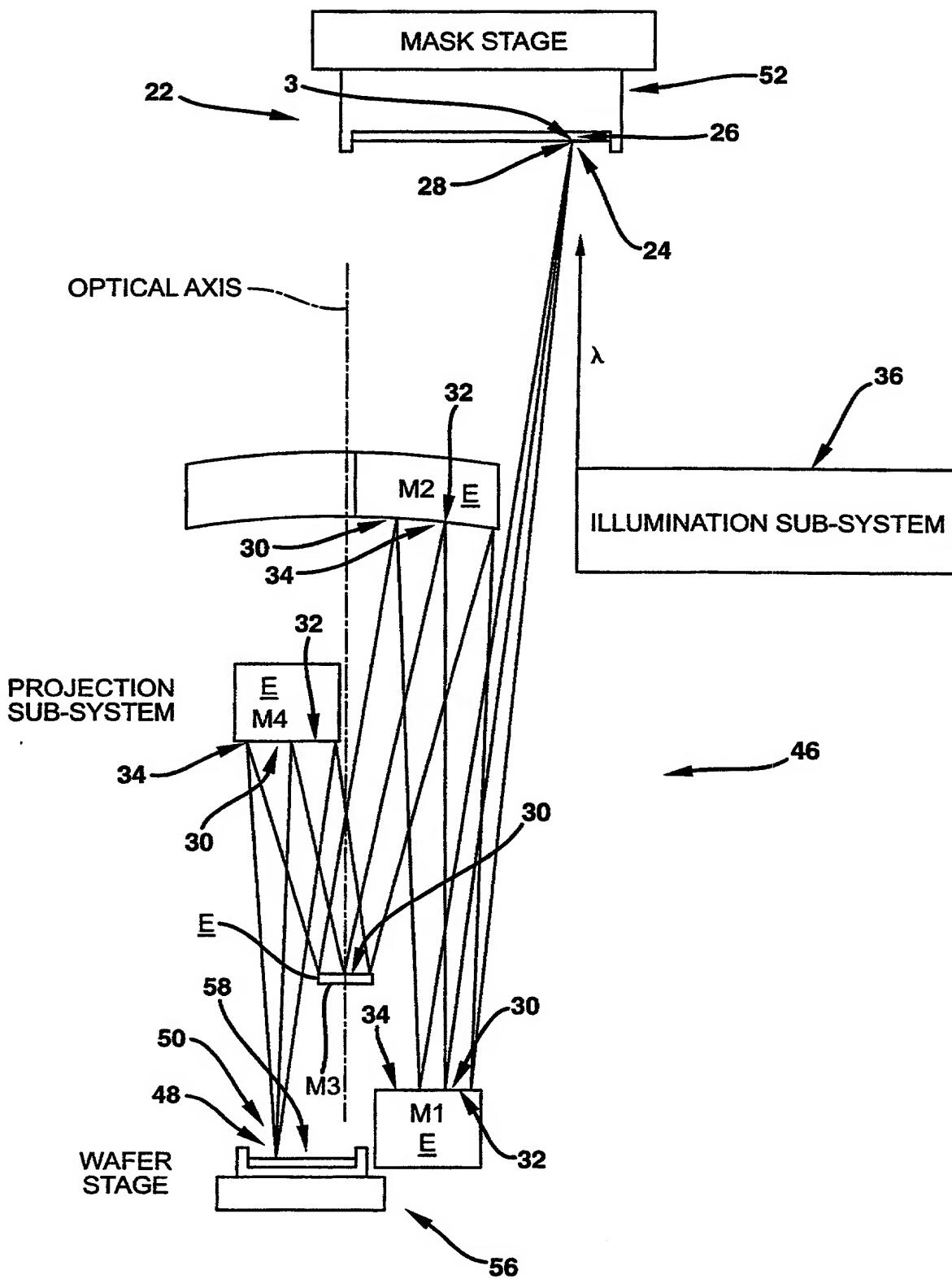
25          43. A method as claimed in claim 39, wherein said glass preform consists of silicon, titanium, and oxygen.

**FIG. 1**



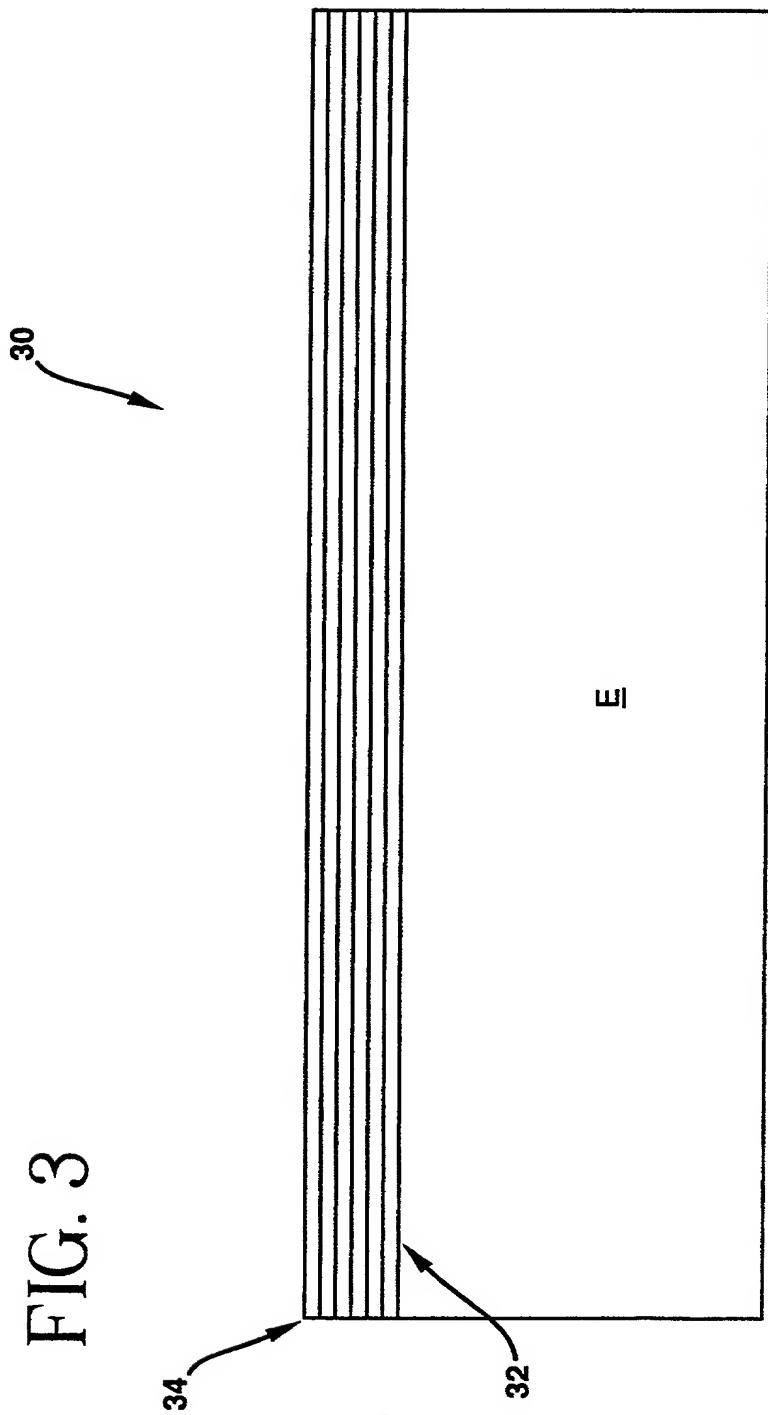
2/12

FIG. 2



3/12

FIG. 3



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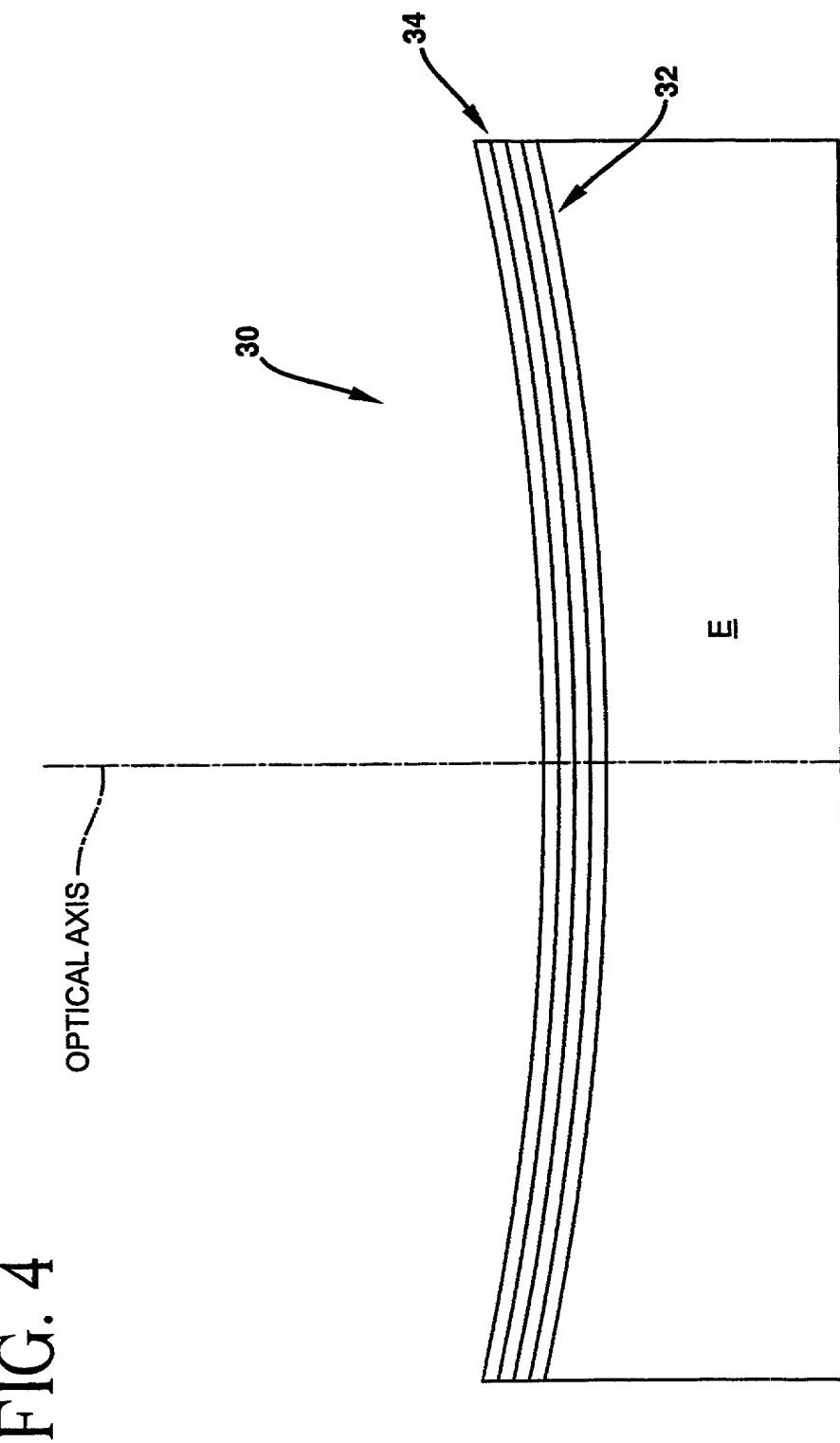
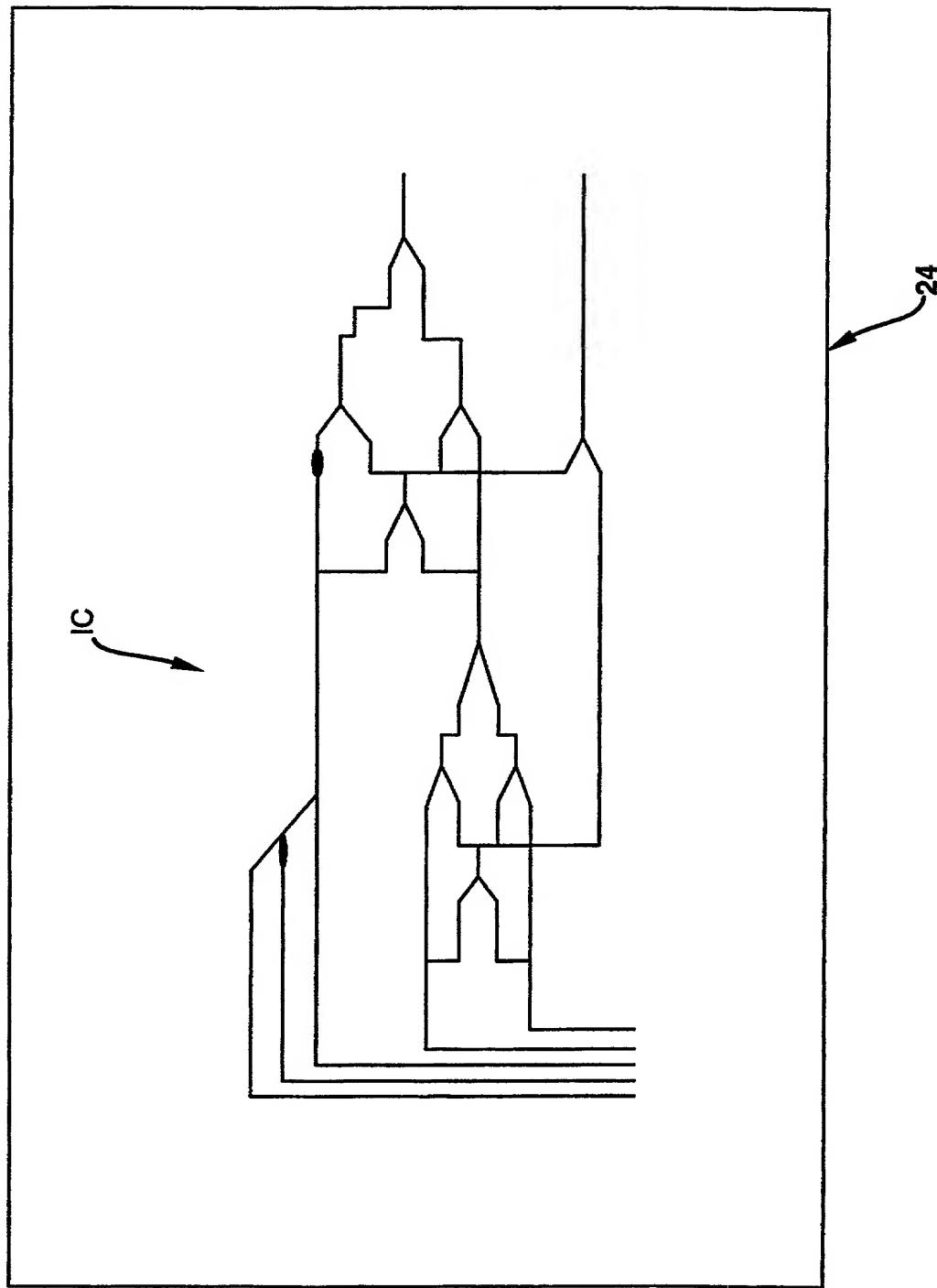


FIG. 4

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FIG. 5a



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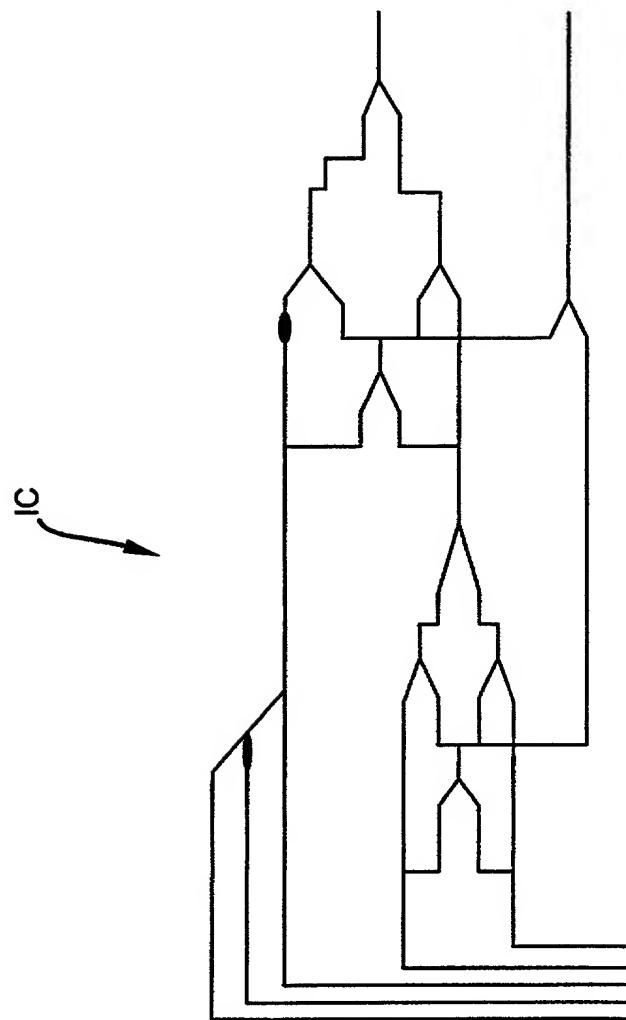


FIG. 5b

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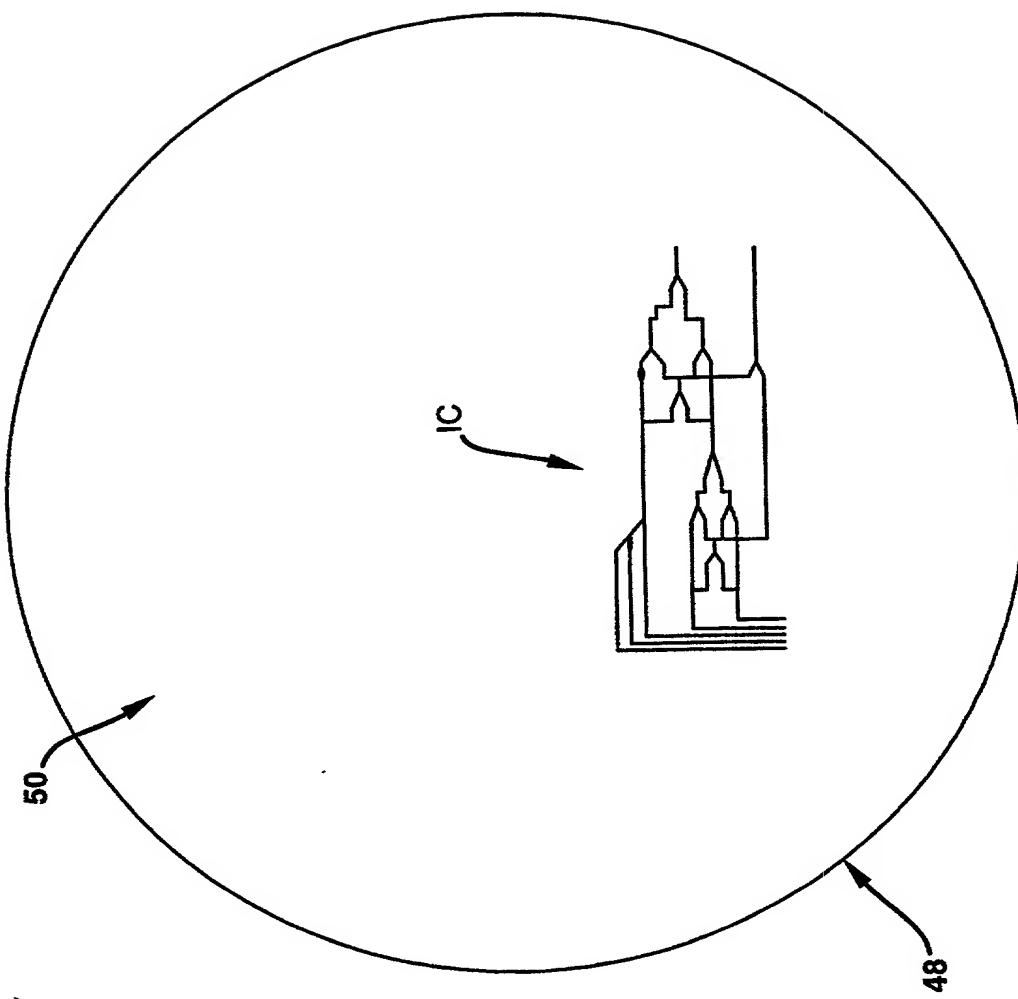


FIG. 5C

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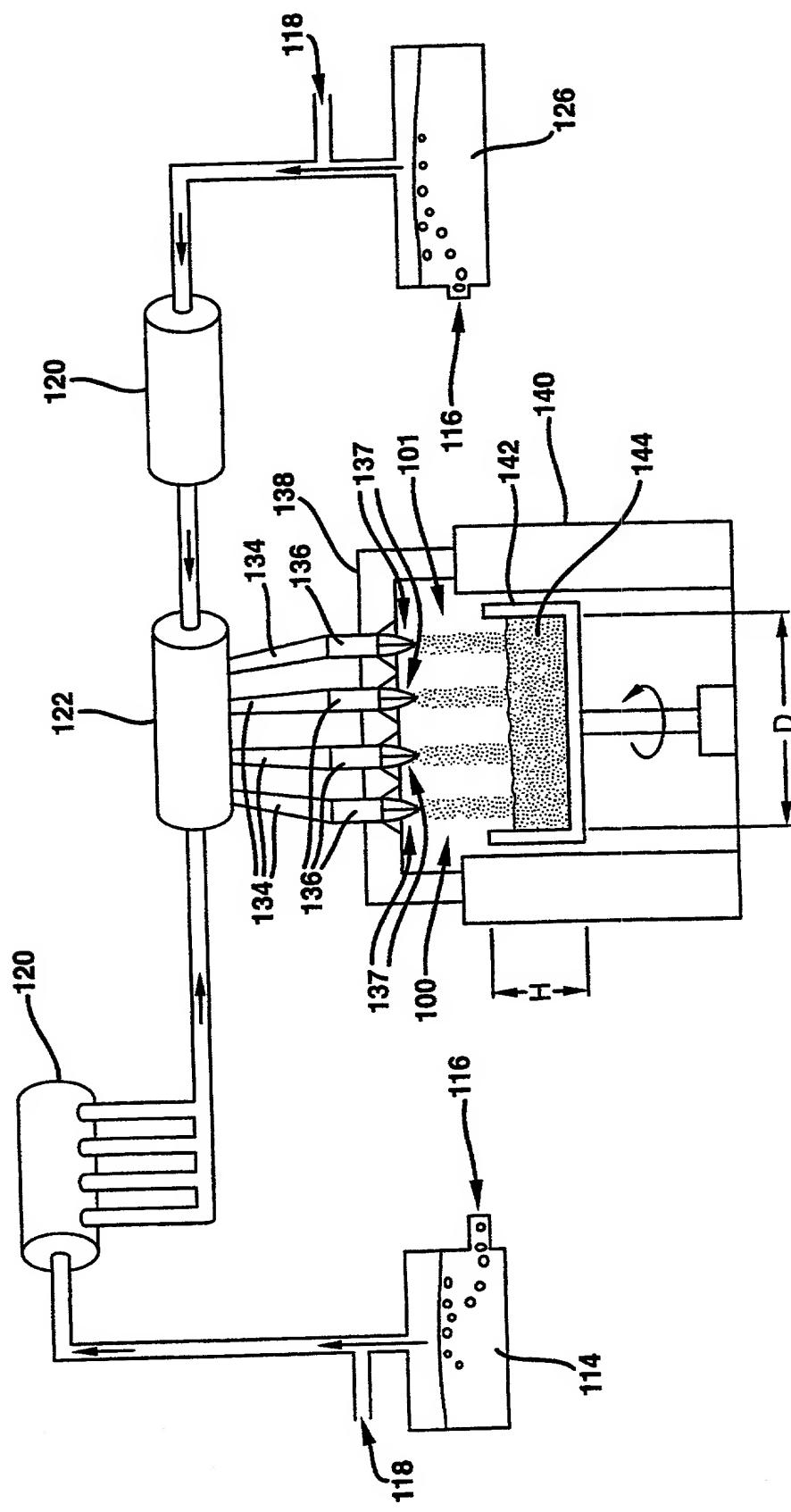


FIG. 6

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FIG. 7a

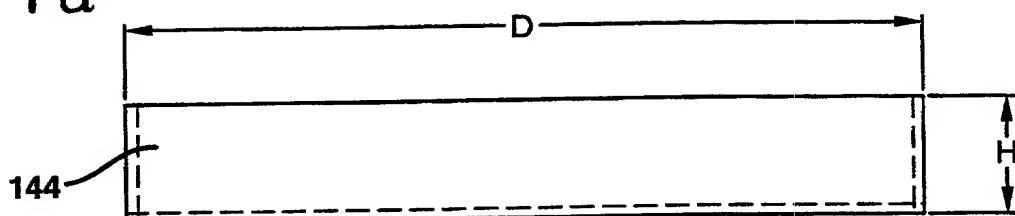


FIG. 7b

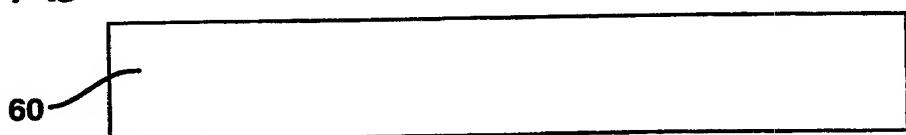


FIG. 7c



FIG. 7A

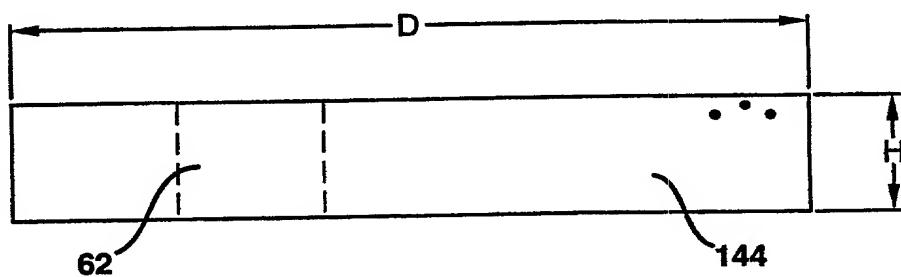


FIG. 7B

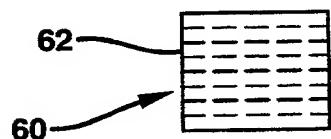
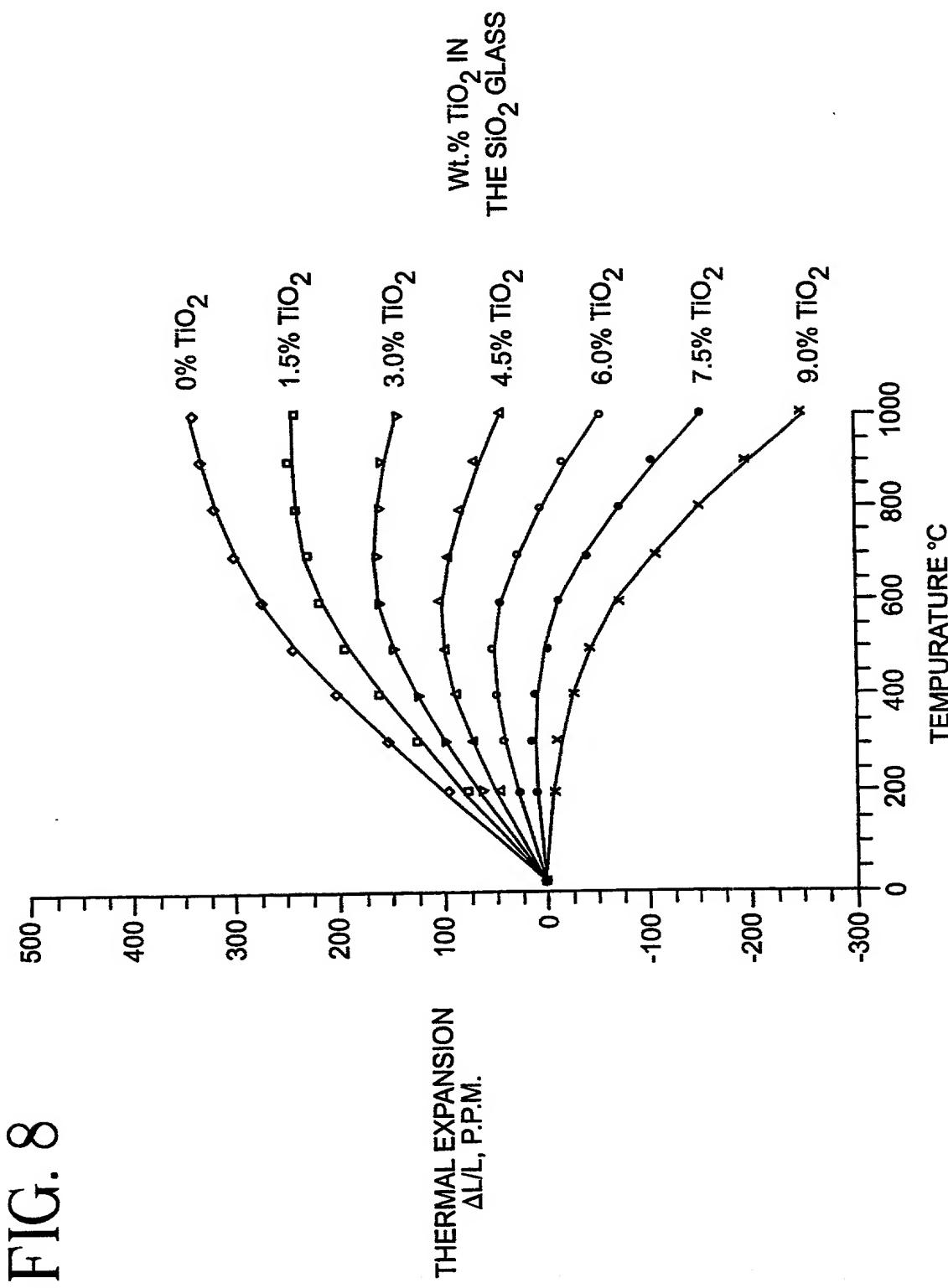


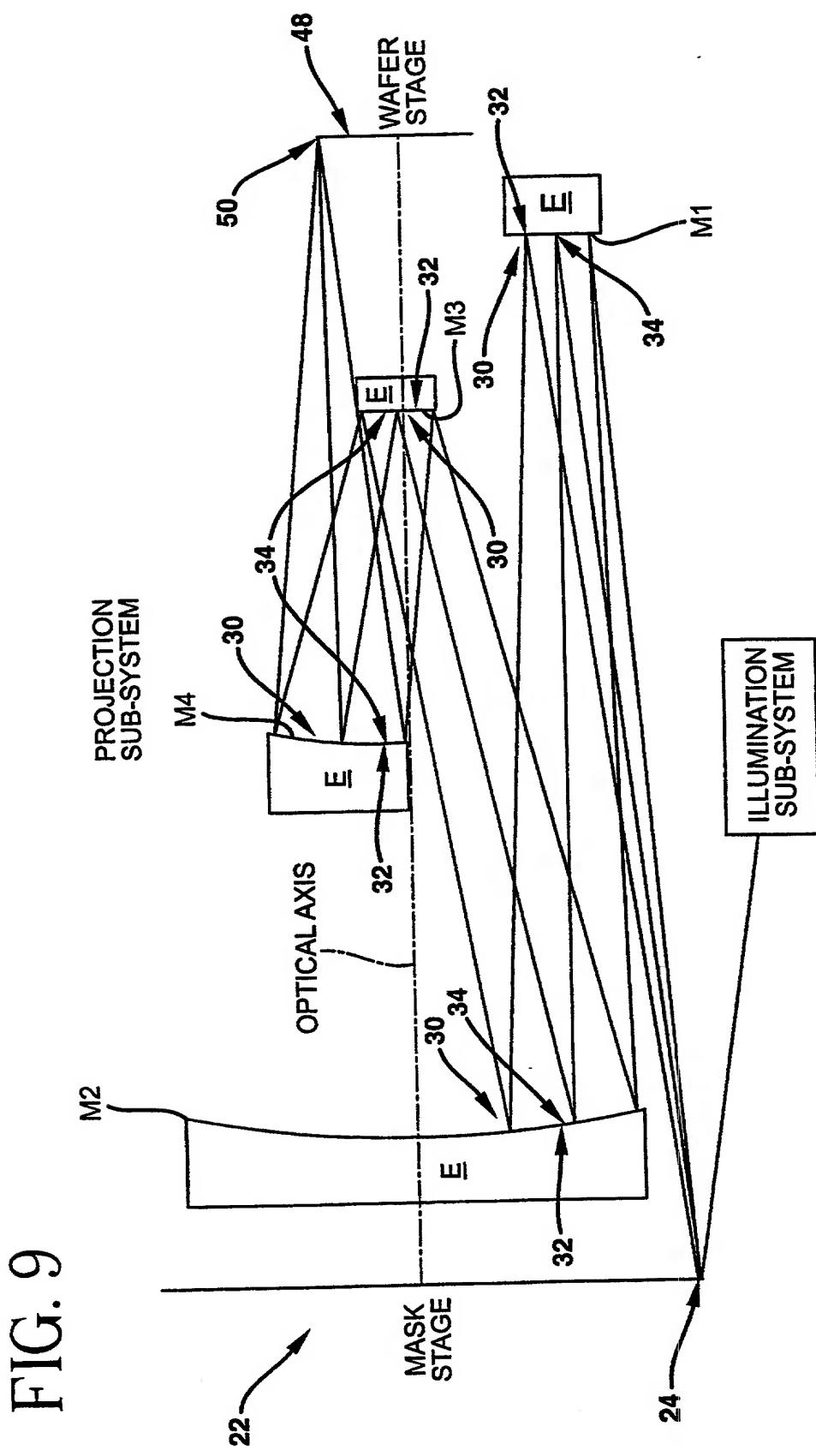
FIG. 7C



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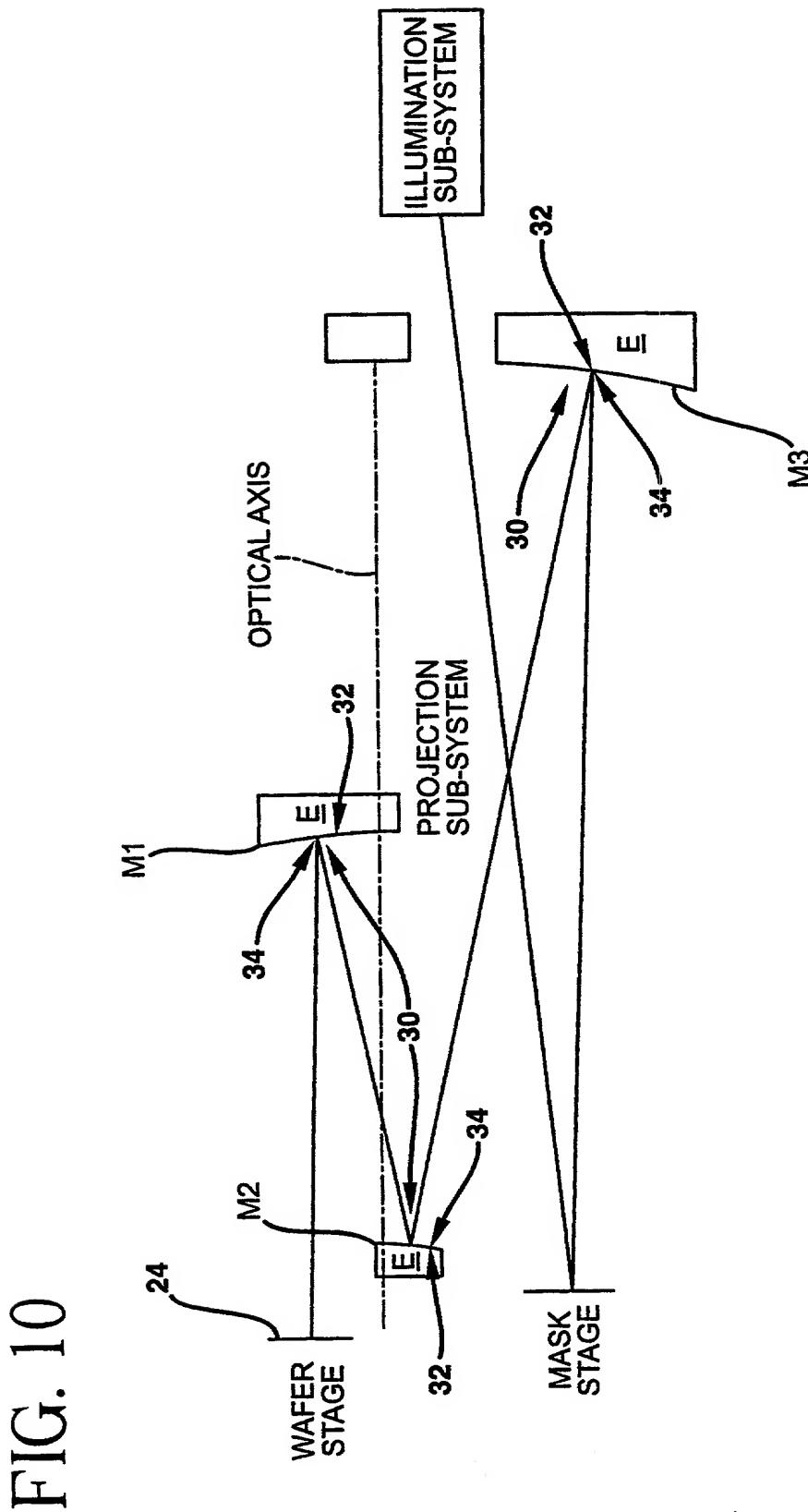


FIG. 10

# DECLARATION IN ORIGINAL APPLICATION

US Attorney Docket No.: SP00-191

As a below named inventor, I declare that:

My residence, Post Office address and citizenship are as stated below next to my name.

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled **EXTREME ULTRAVIOLET SOFT X-RAY PROJECTION LITHOGRAPHIC METHOD SYSTEM AND LITHOGRAPHY ELEMENTS**.

The specification of which (check only one item below):

- is attached hereto
- was filed as United States Application Serial No. on \_\_\_\_\_ and was amended on \_\_\_\_\_ (if applicable)
- was filed as PCT international application number PCT/US00/18798, on 10 July 2000, and was amended under PCT Article 19 on \_\_\_\_\_ (if applicable).

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose information which is material to the examination of this application in accordance with Title 37, Code of Federal Regulations, § 1.56.

I hereby claim foreign priority benefits under Title 35, United States Code, § 119(a)-(d) or 365(b) of any foreign application(s) for patent or inventor's certificate or 365(a) of any PCT international application which designated at least one country other than the United States, listed below and have also identified below any foreign application for patent or inventor's certificate, on the same subject matter, having a filing date before that of the application on which priority is claimed:

Country: \_\_\_\_\_ Application No.: \_\_\_\_\_ Filing Date: \_\_\_\_\_  
 NONE

I hereby claim the benefit under Title 35 United States Code § 119(e) and § 120 of any United States application(s) or 365(c) of any PCT international application designating the United States listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of Title 35 United States Code § 112, I acknowledge the duty to disclose material information as defined in Title 37 Code of Federal Regulations, § 1.56 which occurred between the filing date of the prior application and the national or PCT international filing date of this application:

<input checked="" type="checkbox"/>	Provisional No.: 60/145,057	Filed: 22 July 1999	Status: Lapsed
<input checked="" type="checkbox"/>	Provisional No.: 60/149,840	Filed: 19 August 1999	Status: Lapsed
<input checked="" type="checkbox"/>	Provisional No.: 60/158,813	Filed: 12 October 1999	Status: Lapsed
<input checked="" type="checkbox"/>	PCT Application No: PCT/US00/18798	Filed: 10 July 2000	Status: Pending

**DECLARATION IN ORIGINAL APPLICATION**

US Attorney Docket No.: SP00-191

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

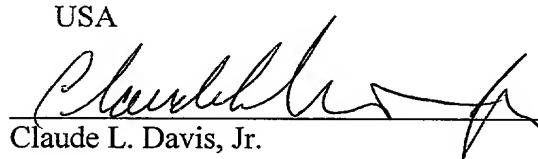
**1-00 Full Name of Inventor:**Claude L. Davis, Jr.**Resident Address:**3812 Meads Creek Road, Painted Post, New York 14870**Post Office Address:**

same

NY

**Citizenship:**

USA

**DATE:** 21 JAN 02

Claude L. Davis, Jr.

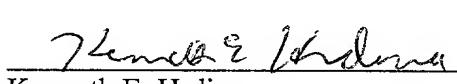
**2-00 Full Name of Inventor:**Kenneth E. Hrdina**Resident Address:**8 Turnberry Drive, Horseheads, New York 14845**Post Office Address:**

same

NY

**Citizenship:**

USA

**DATE:** 01/18/02

Kenneth E. Hrdina